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L4	l2 near2 L3	49	L4
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Ying Chen; Abts, D.; Lilja, D.J.;
Rapid System Prototyping, 2004. Proceedings. 15th IEEE International Workshop on
28-30 June 2004 Page(s):74 - 77
Digital Object Identifier 10.1109/IWRSP.2004.1311099

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☐ 2. **DFS: a de-fragmented file system**
Woo Hyun Ahn; Kyungbaek Kim; Yongjin Choi; Daeyeon Park;
Modeling, Analysis and Simulation of Computer and Telecommunications Systems, 2002.
MASCOTS 2002. Proceedings. 10th IEEE International Symposium on
11-16 Oct. 2002 Page(s):71 - 80
Digital Object Identifier 10.1109/MASCOT.2002.1167062

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☐ 3. **Distributed file systems: focus on Andrew File System/Distributed File Service (AFS/DFS)**
Tobbicke, R.;
Mass Storage Systems, 1994. 'Towards Distributed Storage and Data Management Systems.'
First International Symposium. Proceedings., Thirteenth IEEE Symposium on
12-16 June 1994 Page(s):23 - 26
Digital Object Identifier 10.1109/MASS.1994.373021

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☐ 4. **A 32-bit PowerPC system-on-a-chip with support for dynamic voltage scaling and dynamic frequency scaling**
Nowka, K.J.; Carpenter, G.D.; MacDonald, E.W.; Ngo, H.C.; Brock, B.C.; Ishii, K.I.; Nguyen,
T.Y.; Burns, J.L.;
Solid-State Circuits, IEEE Journal of
Volume 37, Issue 11, Nov. 2002 Page(s):1441 - 1447
Digital Object Identifier 10.1109/JSSC.2002.803941

AbstractPlus | References | Full Text: PDF(494 KB)   IEEE JNL
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☐ 5. **Development of improved methods for remote access of DIII-D data and data analysis**
Greene, K.L.; McHarge, B.B., Jr.;

Fusion Engineering, 1997. 17th IEEE/NPSS Symposium
Volume 2, 6-10 Oct. 1997 Page(s):779 - 782 vol.2
Digital Object Identifier 10.1109/FUSION.1997.687741

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6. **Faster Web page allocation with neural networks**
Phoha, V.V.; Sitharama Iyengar, S.; Kannan, R.;
Internet Computing, IEEE
Volume 6, Issue 6, Nov.-Dec. 2002 Page(s):18 - 26
Digital Object Identifier 10.1109/MIC.2002.1067732

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Inventor Name Search Result

Your Search was:

Last Name = PARK
First Name = GI-HO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08522222	6272622	150	09/01/1995	METHOD OF AND CIRCUIT FOR INSTRUCTION/DATA PREFETCHING USING NON-REFERENCED PREFETCH CACHE	PARK, GI-HO
08923888	5935299	150	09/04/1997	APPARATUS AND METHOD OF SPRAY DRYER/BAG FILTER USING CIRCULATING SHIELD AIR	PARK, GI-HO
09315781	6549983	150	05/20/1999	CACHE MEMORY SYSTEM AND METHOD FOR MANAGING THE SAME	PARK, GI-HO
10631601	7152170	150	07/31/2003	SIMULTANEOUS MULTI-THREADING PROCESSOR CIRCUITS AND COMPUTER PROGRAM PRODUCTS CONFIGURED TO OPERATE AT DIFFERENT PERFORMANCE LEVELS BASED ON A NUMBER OF OPERATING THREADS AND METHODS OF OPERATING	PARK, GI-HO
10801893	Not Issued	80	03/16/2004	Methods, circuits, and systems for utilizing idle time in dynamic frequency scaling cache memories	PARK, GI-HO
11060838	Not Issued	30	02/18/2005	Integrated circuit devices that support dynamic voltage scaling of power supply voltages	PARK, GI-HO
11181144	Not Issued	93	07/13/2005	SEMICONDUCTOR MEMORY DEVICE INCLUDING CIRCUIT TO STORE ACCESS DATA	PARK, GI-HO
11264158	Not Issued	30	11/02/2005	Processor with cache way prediction and method thereof	PARK, GI-HO
11700780	Not Issued	17	02/01/2007	Branch target buffer, a branch prediction circuit and method thereof	PARK, GI-HO
11705485	Not Issued	17	02/12/2007	Accessing semiconductor memory device according to an address and additional access information	PARK, GI-HO

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